King Fahd University of Petroleum & Minerals
Electrical Engineering Department
Fall 2012 (121), EE 672
Take Home – S2

Name: **Student #:**

Question No. 1

a- Determine the low and high frequency poles.

b- Design the circuit such as the low frequency response ranges from f_l **= 5 Hz~ 15 Hz** and the high frequency response ranges from f_h **= 90 MHz ~ 110 MHz.**
Make your own suggestion in order to achieve the required outcomes.

c- Sketch the magnitude $\left|\frac{V_{out}}{V_S}\right|$ (dB) of the CE amplifier versus frequency.
Hint: Similar to figure 5.71 page 491.